Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1-8. (Cancelled)

9. (New) A passive component including a plurality of internal electrodes and one or more terminals, said plurality of internal electrodes serving as a passive circuit formed in a dielectric substrate comprising a plurality of stacked dielectric layers, said one or more terminals being disposed in an outer surface of said dielectric substrate;

wherein said internal electrodes corresponding to said one or more terminals are electrically connected to each other through a connecting via hole formed in said dielectric substrate;

wherein all of said one or more terminals are provided only on a lower surface of said dielectric substrate, each of said one or more terminals being formed by a via hole for terminals exposed on a lower surface of said dielectric substrate; and

wherein a diameter of said via hole for terminals is greater than that of said connecting via hole.

10. (New) The passive component according to claim 9, wherein said passive component comprises at least one resonator, and said at least one resonator comprises two electrodes and a via hole connecting said two electrodes, wherein one of said two electrodes forms a short-circuiting end of said at least one resonator, and the other one of said two electrodes forms an open end of said at least one resonator.

11. (New) The passive component according to claim 9, wherein an internal electrode for shielding is formed in said dielectric substrate, and, of said plurality of dielectric layers of said dielectric substrate, a dielectric layer disposed between said internal electrode for shielding and said lower surface of said dielectric substrate has a dielectric constant of $\varepsilon r < 20$.

12. (New) A passive component mounted on a wiring board including at least a shield wiring pattern, said passive component comprising a plurality of internal electrodes and one or more terminals, said plurality of internal electrodes serving as a passive circuit formed in a dielectric substrate comprising a plurality of stacked dielectric layers, said one or more terminals being disposed in an outer surface of said dielectric substrate;

wherein said one or more terminals are input and output terminals of said passive circuit, and all of said one or more terminals are provided only on a lower surface of said dielectric substrate;

wherein an internal electrode for shielding is formed in said dielectric substrate, and said shield wiring pattern of said wiring board faces said lower surface of said dielectric substrate; and

wherein said internal electrode for shielding and said shield wiring pattern of the wiring board are electrically connected to each other through a capacitance.

13. (New) The passive component according to claim 12, wherein, of said dielectric layers of said dielectric substrate, a dielectric layer disposed between said internal electrode for shielding and said lower surface of said dielectric substrate has a dielectric constant of $\varepsilon r > 20$.

14. (New) The passive component according to claim 12, wherein said passive component comprises at least one resonator, and said at least one resonator comprises two electrodes and a via hole connecting said two electrodes, wherein one of said two electrodes forms a short-circuiting end of at least one resonator, and the other one of said two electrodes forms an open end of said at least one resonator.

15. (New) A passive component comprising:

a plurality of internal electrodes constituting a filter formed in a dielectric substrate comprising a plurality of stacked dielectric layers;

a plurality of internal electrodes constituting an unbalanced-to-balanced converter formed in said dielectric substrate;

a terminal of said filter disposed in an outer surface of said dielectric substrate;

a terminal of said unbalanced-to-balanced converter; and

terminals for shielding;

wherein all of said terminals are provided only on a lower surface of said dielectric substrate;

wherein, of said internal electrodes of said filter, a via hole connected to said terminal of said filter is formed closely to a first side surface of said dielectric substrate along said first side surface of said dielectric substrate;

wherein of said internal electrodes of said unbalanced-to-balanced converter, a via hole connected to said terminal of said unbalanced-to-balanced converter is formed closely to a second side surface of said dielectric substrate along said second side surface of said dielectric substrate; and

wherein of said internal electrodes of said filter and said unbalanced-to-balanced converter, via holes connected to said terminals for shielding are formed closely to a third side surface and a fourth side surface of said dielectric substrate along said third and fourth side surfaces of said dielectric substrate.

16. (New) The passive component according to claim 15, wherein said filter comprises at least one resonator, and said at least one resonator comprises two electrodes and a via hole connecting said two electrodes, wherein one of said two electrodes forms a short-circuiting end of said at least one resonator, the other one of said two electrodes forms an open end of said at least one resonator.

17. (New) The passive component according to claim 15, wherein an internal electrode for shielding is formed in said dielectric substrate, and wherein a dielectric layer disposed between said internal electrode for shielding and said lower surface of said dielectric substrate has a dielectric constant of $\varepsilon r < 20$.